ABSTRACT

In a first example content addressable memory (CAM) system, an input bit pattern is compared to a plurality of identical stored bit patterns. The CAM system generates a hit signal when a match is found for at least one of the identical stored bit patterns. As a result, the system generates a false miss signal only if false matches result for all the identical stored bit patterns. In alternative examples, the system output generates a hit signal when at least half of the identical stored bit patterns match the input bit patterns, or alternatively when a majority of the identical stored bit patterns match the input bit patterns.

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